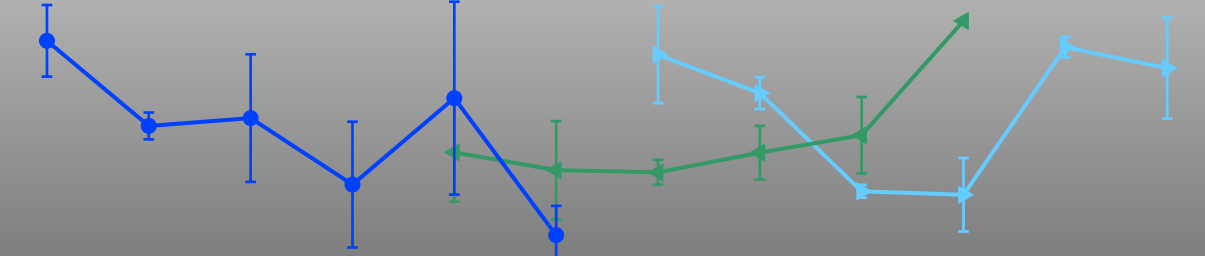


Log2 (expression)

7

6

SD ZT0
SD ZT4
SD ZT8
SD ZT12
SD ZT16
SD ZT20
LD-advance 0
LD-advance 4
LD-advance 8
LD-advance 12
LD-delay ZT0
LD-delay ZT4
LD-delay ZT8
LD-delay ZT12
LD-delay ZT16
LD-delay ZT20



7

6